

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	58229	semiconductor and (nonvolatile or (non adj volatile))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/24 23:19
L2	7275	1 and floating and control and gate and electrode	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/24 23:19
L3	2043	2 and (trench\$2 or groove or recess\$2)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/24 23:20
L4	1840	3 and (dielectric or insulat\$3) <i>S.L.</i>	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB <i>S.L.</i>	OR <i>S.L.</i>	ON <i>S.L.</i>	2005/07/24 23:20 <i>S.L.</i>

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4486	(257/314,315,316,324).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/07/24 23:59
L2	809	(438/258).CCLS.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	OFF	2005/07/24 23:59